AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings of claims in the Application.

Listing of Claims:

Claim 1 (Currently amended): A data buffer circuit for a video decoder comprising:

a receiver circuit operable to receive a video bitstream;

a ring buffer operable to receive the video bitstream, the ring buffer including a predetermined number of memory locations, the video bitstream being stored in selectively either sequentially increasing or sequentially decreasing addresses as received from the receiver circuit, data stored in the ring buffer being accessible by ~~both~~ selectively either sequentially increasing memory addresses and sequentially decreasing memory addresses; and

an error resilience module operable to select ~~either~~ an error correction procedure from a plurality of error correction procedures ~~or to select no error correction procedure~~, the ~~selection responsive to~~ error correction procedure corresponding to an error in the video bitstream stored in the ring buffer as determined by analysis ~~of the video bitstream~~ thereof in both a forward direction and a reverse direction ~~as accessed via the ring buffer~~.

Claim 2 (Previously presented): The data buffer circuit as recited in Claim 1, wherein the receiver circuit comprises a wireless receiver.

Claim 3 (Previously presented): The data buffer circuit as recited in Claim 1, further comprising a log interface circuit operable to store data logging information in the ring buffer such that the data logging information is aligned with corresponding data from the video bitstream.

Claim 4 (Previously presented): The data buffer circuit as recited in Claim 1, further comprising a VOP decoder interposed in a data flow between the receiver circuit and the ring buffer such that the video bitstream stored in the ring buffer is in a decoded form.

Claim 5 (Currently amended): A data buffer circuit for a video decoder comprising:

means for receiving a video bitstream;

means for inspecting the video bitstream for error and providing an error indication;

means for storing the video bitstream in a ring buffer ~~subsequent to the video bitstream being inspected for error and~~ prior to correcting errors therein ~~correction thereof, the video bitstream being stored in the ring buffer as video~~

~~bitstream data by the video bitstream storing means~~ at selectively either sequentially increasing addresses thereof or at sequentially decreasing addresses thereof, the video bitstream data in the ring buffer being accessible selectively by one of: sequentially increasing memory addresses thereof, sequentially decreasing memory addresses thereof or by random access to a specified memory address thereof;

means for storing data logging information corresponding to the video bitstream data in the ring buffer, the data logging information being aligned in memory with the corresponding video bitstream data; and

means for ~~automatically~~ concurrently retrieving both a portion of the video bitstream data from the ring buffer and a corresponding portion of the data logging information from the ring buffer responsive to a request for retrieving the portion of the video bitstream data from the ring buffer.

Claim 6 (Currently amended): A method of accessing information from a video bitstream comprising:

receiving a video bitstream;

inspecting the video bitstream for error;

storing the video bitstream in a ring buffer as video bitstream data~~, the storage occurring subsequent to the video bitstream inspecting step and~~ prior to correcting any error therein, the video bitstream data in the ring buffer being

stored selectively by one of: accessing sequentially increasing addresses thereof or accessing sequentially decreasing addresses thereof; ~~encountered thereby~~;

storing in the ring buffer data logging information corresponding to the video bitstream data ~~in the ring buffer~~, the data logging information being aligned in memory with the corresponding video bitstream data; and

concurrently ~~automatically~~ retrieving both a portion of the video bitstream data from the ring buffer and a corresponding portion of the data logging information from the ring buffer responsive to a request for retrieving the portion of the video bitstream data from the ring buffer, the video bitstream data in the ring buffer being retrieved selectively by one of: accessing sequentially increasing memory addresses thereof, accessing sequentially decreasing memory addresses thereof or by random access to a specified memory address thereof.

Claim 7 (Previously presented): The method as recited in claim 6, further comprising the step of wirelessly receiving the video bitstream.

Claim 8 (Previously presented): The method as recited in claim 6, further comprising the step of receiving the video bitstream in an MPEG-4 compliant decoder.

Claim 9 (Previously presented): The method as recited in claim 6, further comprising the steps of:

decoding video object planes (VOPs) from the video bitstream prior to storing the video bitstream in the ring buffer; and

storing in the ring buffer the decoded VOPs as the video bitstream data in the video bitstream storing step.

Claim 10 (Previously presented): The method as recited in claim 6, further comprising the steps of:

storing in the ring buffer video object planes (VOPs) as the video bitstream data in the video bitstream storing step;

retrieving the VOPs from the ring buffer upon demand therefor; and

decoding the VOPs subsequent to the retrieval thereof from the ring buffer.